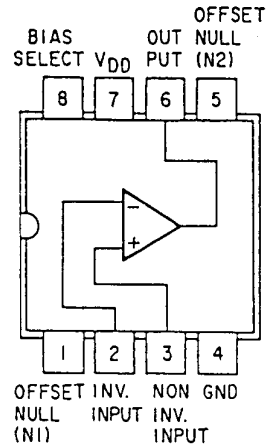
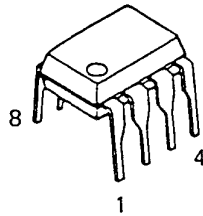
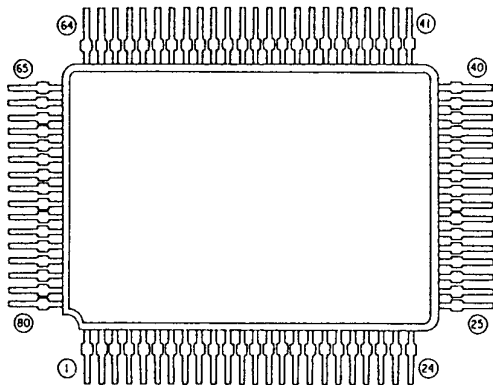


TLC 271CP



UC1143



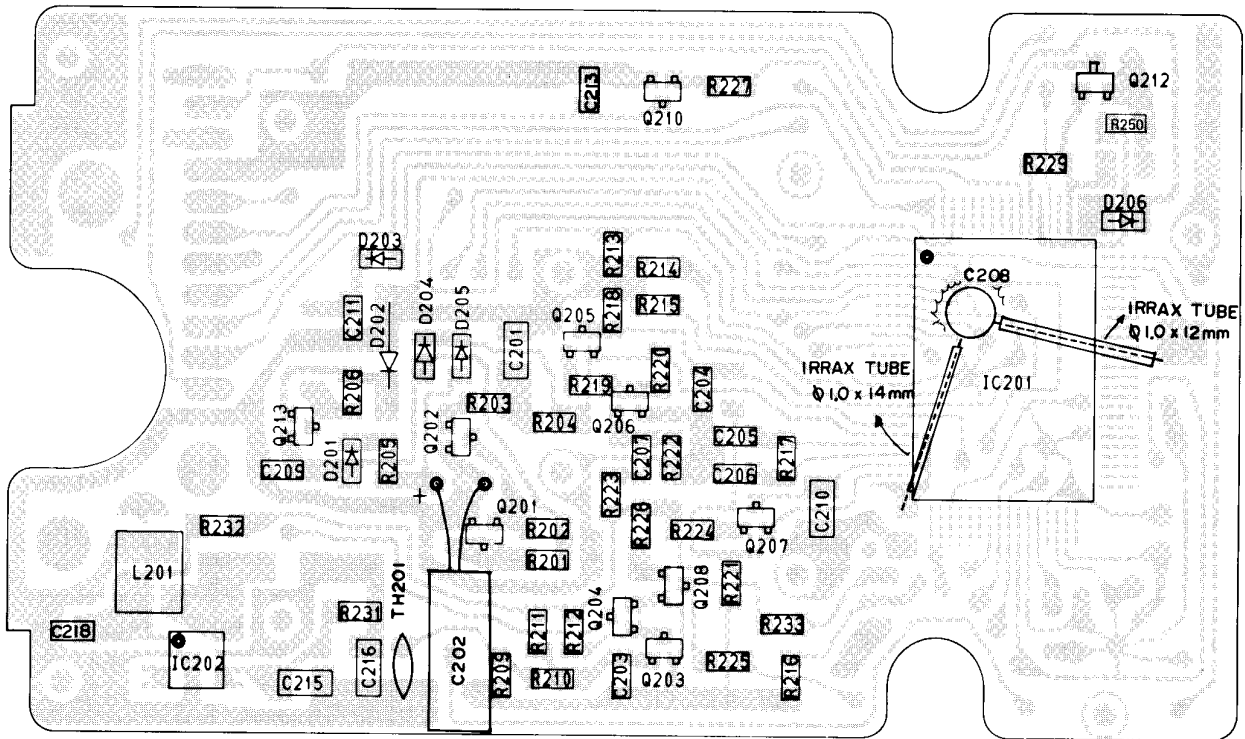
UC1143 Pin Connection

PIN NO.	TERMINAL DESCRIPTION (Input/Output Level, Interface)	PIN NO.	TERMINAL DESCRIPTION (Input/Output Level, Interface)
1	Not used	42	
2		43	
3		44	
4		45	
5	} Key Matrix	46	
6		47	
7		48	
8		49	
9		50	
10	BEEP output, Pin 68: BEEP output at "L"	51	} Not Used
11		52	
12	} Not Used	53	
13		54	
14		55	
15	} Key Matrix	56	
16		57	
17	Key Lock SW I/O Terminal "H": KEY LOCK ON (KEY input is not available)	58	
18	"L": RESET OFF	59	
19	"H": RESET ON	60	
20	Always connected to VCC	61	
21		62	
22	Microcomputer Clock	63	GND
23	Power Terminal (4.5~5.5V)	64	POWER FALL Detector Input "L": Memory clear with RST ON
24	"L": HALT ON	65	SC "H": SCAN STOP "L": SCAN
25	"H": HALT OFF	66	Europe Band Select "H": U.S.A. Band "L": Europe Band
26	} LCD Bias Setting Terminal	67	Not Used
27		68	BATTERY "L": BEEP output from pin 10 LOW "H": BEEP OFF
28	} Not Used	69	TEST Frequency Select "L" Normal
29		70	(VHF)
30		71	Band Select Output (UHF)
31	} LCD Segment Terminal	72	
32		73	
33		74	} Not Used
34		75	
35		76	
36		77	
37		78	(EN)
38		79	PLL CONTROL (DATA)
39		80	(CLOCK)
40			
41	Not Used		

KEY BOARD P.C. BOARD

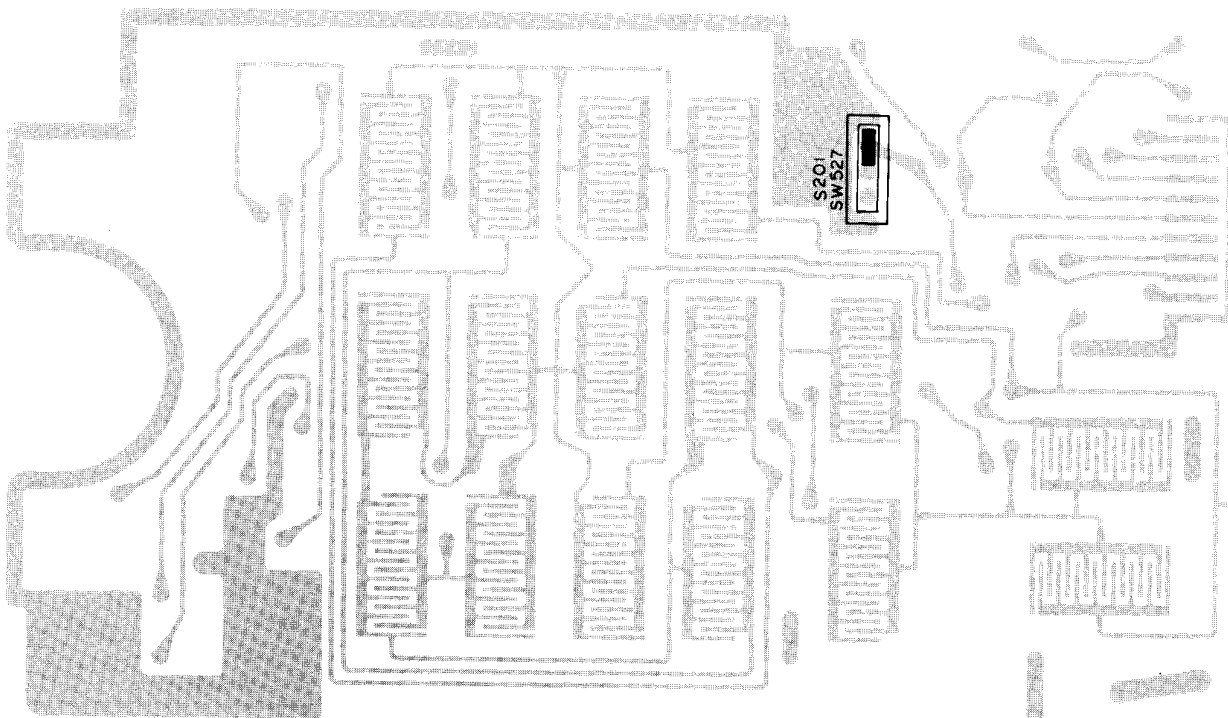
(TOP VIEW)

B201 PH-032AB



(BOTTOM VIEW)

B201 PH-032AB (BOTTOM VIEW)



WIRING DIAGRAM

UB106B:150:2

